



FIG. 3A

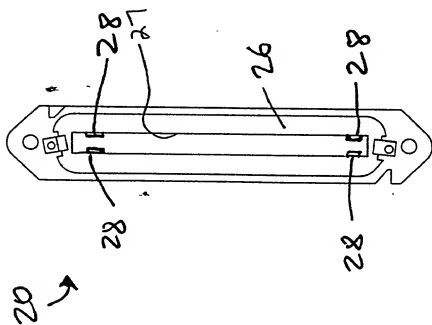
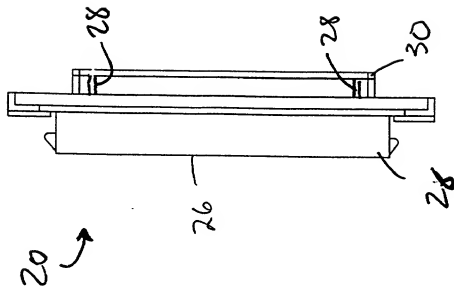


FIG. 3B



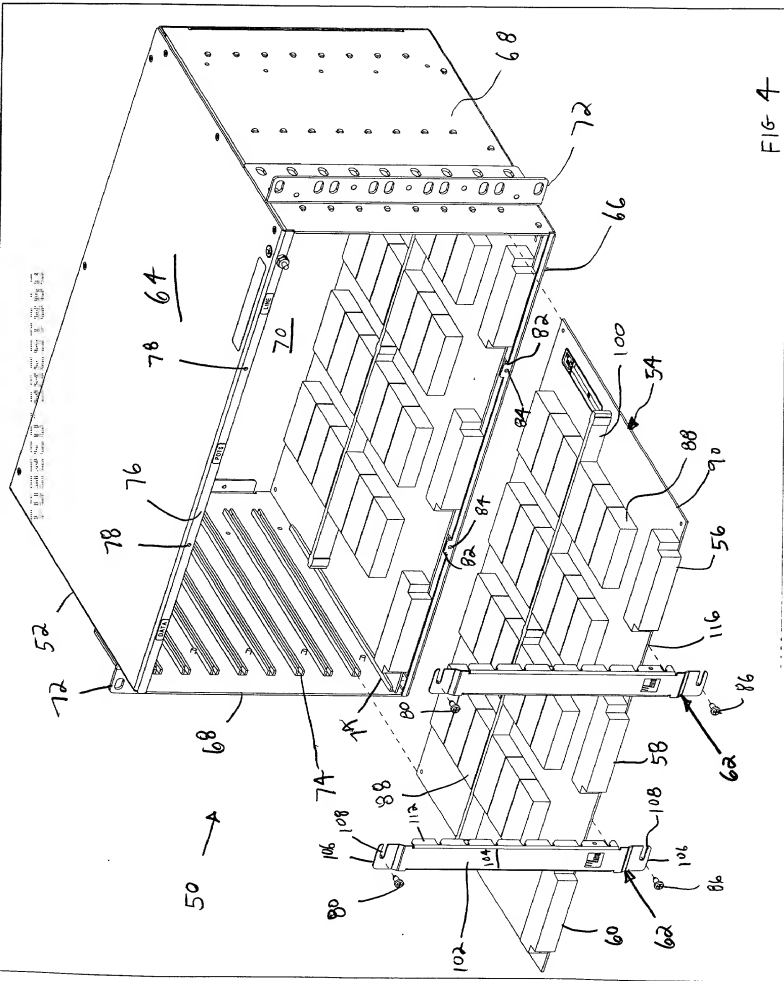


FIG. 4

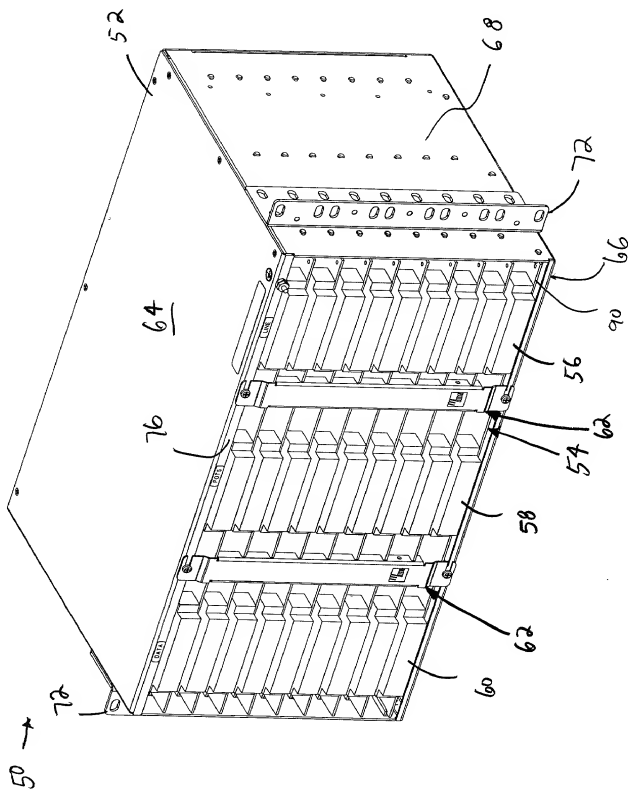


FIG. 5

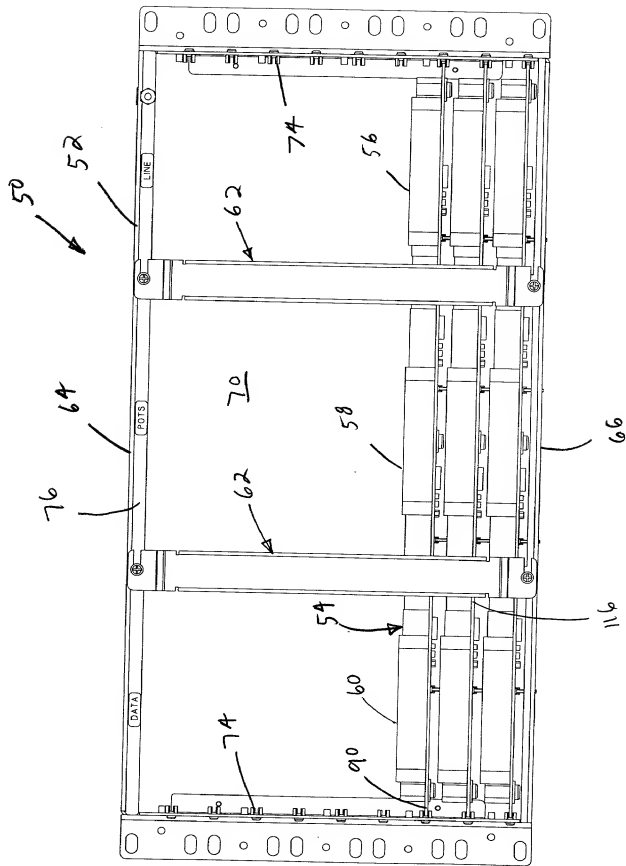


FIG. 6

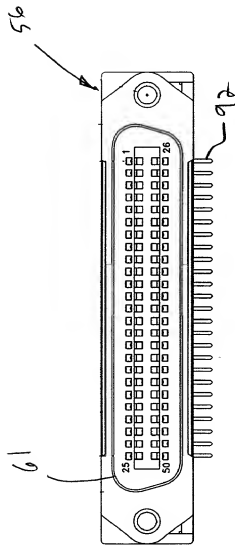


FIG. 7A

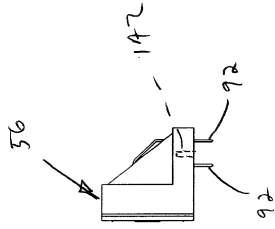


FIG. 7B

The diagram illustrates a circuit board layout with the following components and connections:

- Interface Components (Left Side):**
 - LINE (26):** A component at the bottom left, connected to a bus (96) and a memory module (94).
 - POTS (26):** A component in the middle left, connected to the same bus (96) and a memory module (94).
 - DATA (26):** A component at the top left, connected to a bus (98) and a memory module (94).
- Memory Modules (94):** A series of modules arranged in three rows on the right side of the board. Each module contains a grid of cells, some of which are marked with a dot.
- Bus Systems:**
 - Bus 96:** A horizontal bus connecting the LINE and POTS components to the memory modules.
 - Bus 98:** A horizontal bus connecting the DATA component to the memory modules.
- Other Labels:**
 - 56:** Points to the LINE component.
 - 58:** Points to the POTS component.
 - 60:** Points to the DATA component.
 - 88:** Points to the memory modules.
 - 96:** Points to the bus connecting LINE and POTS.
 - 98:** Points to the bus connecting DATA.

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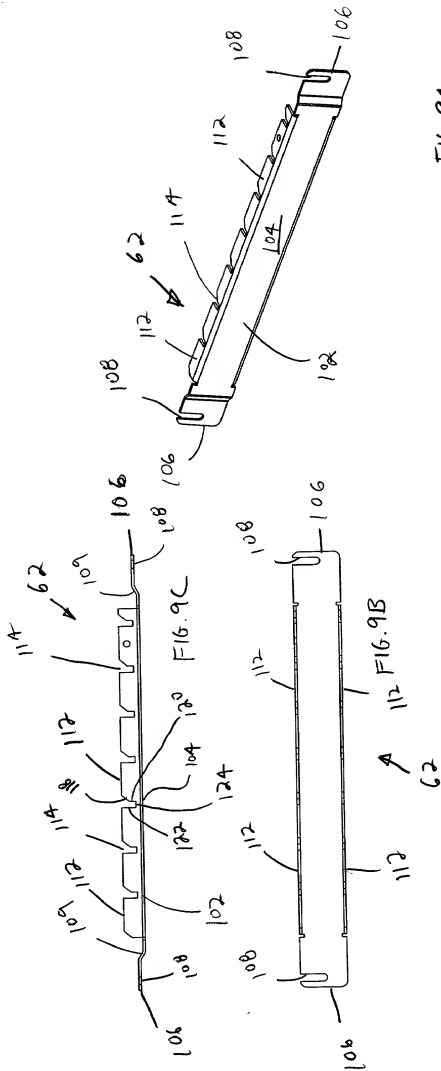


FIG. 9A

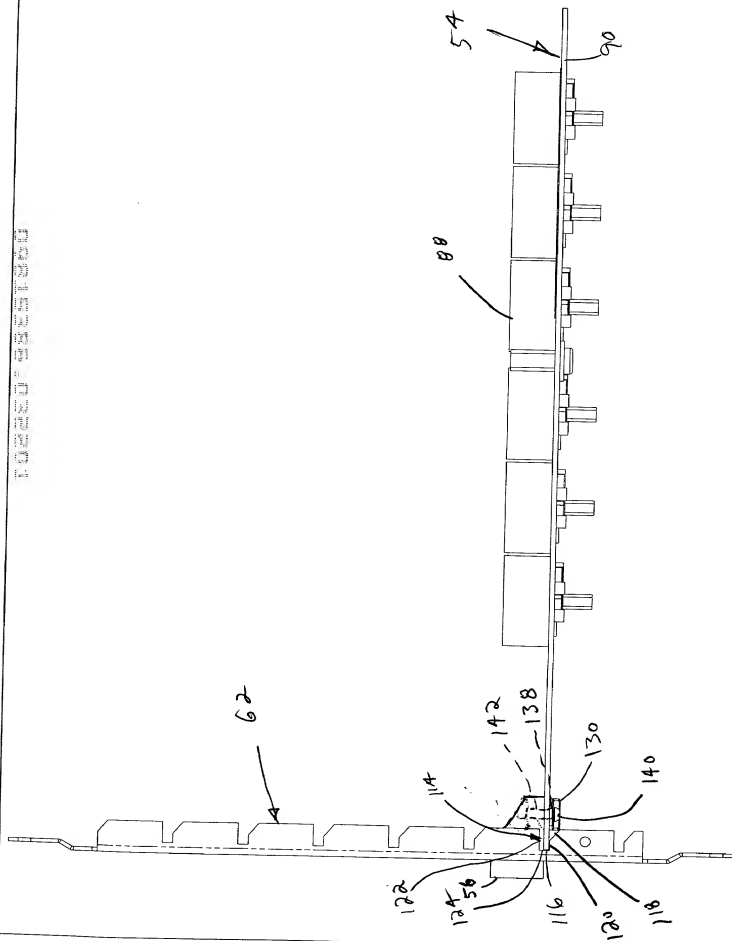


FIG. 10B

